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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Satoru MOTOYAMA

Serial No.: Currently unknown

Filing Date: Concurrently
herewith

Title: TEMPORARY STORAGE OF
COMMUNICATIONS DATA

## COMMENTARY RE INFORMATION DISCLOSURE STATEMENT

Box Patent Application Assistant Commissioner for Patents Washington, D. C. 20231

Sir:

In accordance with 37 CFR §1.56, and in recognition of his duty to disclose to the United States Patent and Trademark Office relevant information known to be material to patentability, Applicant herewith submits copies of the prior art listed on the attached Information Disclosure Statement (Form PTO-1449). The statement is not a representation that all of the information cited is necessarily effective as prior art against the application. The relevant documents are as follows:

1. Japanese Patent Publication No. 4-306387 dated July 23, 1992, discloses an automatic performance device. The device can be designated a first mode or a second mode. The device writes performance information generated by a keyboard to a first memory device and reads performance information from a

second memory device in parallel in the first mode, and reads performance information from the first memory device and writes performance information generated by the keyboard to the second memory device in parallel in the second mode. The device carries out performance in accordance with the performance information of the second memory device in the first mode. A performer can perform a keyboard with the performance of the first mode. The performance information of the keyboard can be memorized in the first memory device. The device carries out performance in accordance with the performance information of the first memory device in the second mode. The performer can perform a keyboard with the performance of the second mode. The performance information of the keyboard can be memorized in the second memory device; and

2. Japanese Patent Publication No. 6-51762 dated February 25, 1994, discloses an automatic performance device. The device receives performance data generated by a performance operator and performance data read from RAM, automatically carries out performance in accordance with the performance data, writes the performance data in a buffer track area in RAM, and rewrites the data in a performance data memory area after automatic performance.

Applicant respectfully requests that the enclosed documents be made of record in the subject application.

Respectfully submitted,

Date: <u>March 10, 1998</u>

Brian M. Berliner

Registration No. 34,549 Attorney for Applicant(s)

GRAHAM & JAMES LLP 801 S. Figueroa Street, 14th Floor Los Angeles, California 90017-5554 (213) 624-2500